

Docket No.: 060188-0662



**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Ryota NISHIKAWA, et al.

Application No.: 10/679,365

Filed: October 07, 2003

Response Under 37 CFR 1.116 - Expedited Procedure

Customer Number: 20277

Confirmation Number: 3864

Group Art Unit: 2836

Examiner: Deberadinis, Robert L.

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD FOR DESIGNING THE SAME

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☐ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☐ Also attached:

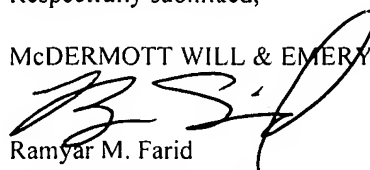
The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	30	35	0	\$50.00 =	\$0.00
Independent Claims	9	3	6	\$200.00 =	\$1200.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$1200.00

- ☒ Please charge my Deposit Account No. 500417 in the amount of \$1200.00.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

  
Ramyar M. Farid  
Registration No. 46,692

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
Phone: 202.756.8000 RMF:MaM  
Facsimile: 202.756.8087  
Date: December 18, 2006

Please recognize our Customer No. 20277 as our correspondence address.



Docket No.: 060188-0662

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Ryota NISHIKAWA, et al.

Application No.: 10/679,365

Filed: October 07, 2003

**Response Under 37 CFR 1.116 - Expedited Procedure**

: Customer Number: 20277

: Confirmation Number: 3864

: Group Art Unit: 2836

: Examiner: Robert L. Deberadinis

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD FOR  
DESIGNING THE SAME

**AMENDMENT UNDER 37 CFR 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated September 18, 2006, having a shortened statutory period for response set to expire December 18, 2006, please amend the above-identified application as follows:

12/19/2006 BARRAHAI 00000122 500417 10679365  
01 FC:1201 1200.00 DA